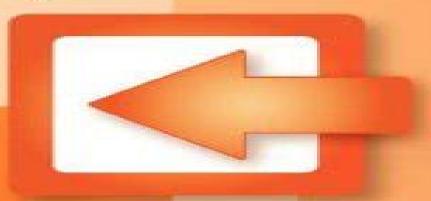# Embedded System Design

Modeling, Synthesis and Verification

Daniel D. Gajski
Samar Abdi
Andreas Gerstlauer
Gunar Schirner

Embedded System Design Modeling Synthesis And Verification

Brian Bailey,Grant Martin

Embedded System Design Modeling Synthesis And Verification:

   Embedded System Design Daniel D. Gajski,Samar Abdi,Andreas Gerstlauer,Gunar Schirner,2009-08-14 Embedded System Design Modeling Synthesis and Verification introduces a model based approach to system level design It presents modeling techniques for both computation and communication at different levels of abstraction such as specification transaction level and cycle accurate level It discusses synthesis methods for system level architectures embedded software and hardware components Using these methods designers can develop applications with high level models which are automatically translatable to low level implementations This book furthermore describes simulation based and formal verification methods that are essential for achieving design confidence The book concludes with an overview of existing tools along with a design case study outlining the practice of embedded system design Specifically this book addresses the following topics in detail System modeling at different abstraction levels Model based system design Hardware Software codesign Software and Hardware component synthesis System verification This book is for groups within the embedded system community students in courses on embedded systems embedded application developers system designers and managers CAD tool developers design automation and system engineering   **Out-of-order Parallel Discrete Event Simulation for Electronic System-level Design** Weiwei Chen,2014-07-24 This book offers readers a set of new approaches and tools a set of tools and techniques for facing challenges in parallelization with design of embedded systems It provides an advanced parallel simulation infrastructure for efficient and effective system level model validation and development so as to build better products in less time Since parallel discrete event simulation PDES has the potential to exploit the underlying parallel computational capability in today s multi core simulation hosts the author begins by reviewing the parallelization of discrete event simulation identifying problems and solutions She then describes out of order parallel discrete event simulation OoO PDES a novel approach for efficient validation of system level designs by aggressively exploiting the parallel capabilities of todays multi core PCs This approach enables readers to design simulators that can fully exploit the parallel processing capability of the multi core system to achieve fast speed simulation without loss of simulation and timing accuracy Based on this parallel simulation infrastructure the author further describes automatic approaches that help the designer quickly to narrow down the debugging targets in faulty ESL models with parallelism   *A Practical Introduction to Hardware/Software Codesign* Patrick R. Schaumont,2012-11-27 This textbook serves as an introduction to the subject of embedded systems design with emphasis on integration of custom hardware components with software The key problem addressed in the book is the following how can an embedded systems designer strike a balance between flexibility and efficiency The book describes how combining hardware design with software design leads to a solution to this important computer engineering problem The book covers four topics in hardware software codesign fundamentals the design space of custom architectures the hardware software interface and application examples The book comes with an associated design

environment that helps the reader to perform experiments in hardware software codesign Each chapter also includes exercises and further reading suggestions Improvements in this second edition include labs and examples using modern FPGA environments from Xilinx and Altera which will make the material in this book applicable to a greater number of courses where these tools are already in use More examples and exercises have been added throughout the book If I were teaching a course on this subject I would use this as a resource and text If I were a student who wanted to learn codesign I would look for a course that at least used a similar approach If I were an engineer or engineering manager who wanted to learn more about codesign from a very practical perspective I would read this book first before any other When I first started learning about codesign as a practitioner a book like this would have been the perfect introduction Grant Martin Tensilica

Intelligent Software Methodologies, Tools and Techniques Hamido Fujita,Guido Guizzi,2015-08-31 This book constitutes the best papers selection from the proceedings of the 14th International Conference on Intelligent Software Methodologies Tools and Techniques SoMeT 2015 held in Naples Italy in September 2015 The 47 full papers presented together with one short paper were carefully reviewed and selected from 118 submissions The papers are organized in topical sections on embedded and mobile software systems theory and application real time systems requirement engineering high assurance and testing system social networks and big data cloud computing and semantic web artificial intelligence techniques and intelligent system design software development and integration security and software methodologies for reliable software design new software techniques in image processing and computer graphics software applications systems for medical health care **Design of Reconfigurable Logic Controllers** Andrei Karatkevich,Arkadiusz Bukowiec,Michał Doligalski,Jacek Tkacz,2015-12-23 This book presents the original concepts and modern techniques for specification synthesis optimisation and implementation of parallel logical control devices It deals with essential problems of reconfigurable control systems like dependability modularity and portability Reconfigurable systems require a wider variety of design and verification options than the application specific integrated circuits The book presents a comprehensive selection of possible design techniques The diversity of the modelling approaches covers Petri nets state machines and activity diagrams The preferences of the presented optimization and synthesis methods are not limited to increasing of the efficiency of resource use One of the biggest advantages of the presented methods is the platform independence the FPGA devices and single board computers are some of the examples of possible platforms These issues and problems are illustrated with practical cases of complete control systems If you expect a new look at the reconfigurable systems designing process or need ideas for improving the quality of the project this book is a good choice g process or need ideas for improving the quality of the project this book is a good choice **Encyclopedia of Parallel Computing** David Padua,2014-07-08 Containing over 300 entries in an A Z format the Encyclopedia of Parallel Computing provides easy intuitive access to relevant information for professionals and researchers seeking access to any aspect within the broad field of parallel computing Topics for this comprehensive reference were

selected written and peer reviewed by an international pool of distinguished researchers in the field The Encyclopedia is broad in scope covering machine organization programming languages algorithms and applications Within each area concepts designs and specific implementations are presented The highly structured essays in this work comprise synonyms a definition and discussion of the topic bibliographies and links to related literature Extensive cross references to other entries within the Encyclopedia support efficient user friendly searchers for immediate access to useful information Key concepts presented in the Encyclopedia of Parallel Computing include laws and metrics specific numerical and non numerical algorithms asynchronous algorithms libraries of subroutines benchmark suites applications sequential consistency and cache coherency machine classes such as clusters shared memory multiprocessors special purpose machines and dataflow machines specific machines such as Cray supercomputers IBM s cell processor and Intel s multicore machines race detection and auto parallelization parallel programming languages synchronization primitives collective operations message passing libraries checkpointing and operating systems Topics covered Speedup Efficiency Isoefficiency Redundancy Amdahls law Computer Architecture Concepts Parallel Machine Designs Benmarks Parallel Programming concepts design Algorithms Parallel applications This authoritative reference will be published in two formats print and online The online edition features hyperlinks to cross references and to additional significant research Related Subjects supercomputing high performance computing distributed computing **Natural Language Processing for Electronic Design Automation** Mathias Soeken,Rolf Drechsler,2020-08-31 This book describes approaches for integrating more automation to the early stages of EDA design flows Readers will learn how natural language processing techniques can be utilized during early design stages in order to automate the requirements engineering process and the translation of natural language specifications into formal descriptions This book brings together leading experts to explain the state of the art in natural language processing enabling designers to integrate these techniques into algorithms through existing frameworks **Embedded System Design: Topics, Techniques and Trends** Achim Rettberg,Mauro Zanella,Rainer Domer,Andreas Gerstlauer,Franz Rammig,2010-05-09 This volume presents the technical program of the 2007 International Embedded Systems Symposium held in Irvine California It covers timely topics techniques and trends in embedded system design including design methodology networks on chip distributed and networked systems and system verification It places emphasis on automotive and medical applications and includes case studies and special aspects in embedded system design *Embedded System Design: Topics, Techniques and Trends* Achim Rettberg,Mauro Zanella,Rainer Domer,Andreas Gerstlauer,Franz Rammig,2007-06-01 This volume presents the technical program of the 2007 International Embedded Systems Symposium held in Irvine California It covers timely topics techniques and trends in embedded system design including design methodology networks on chip distributed and networked systems and system verification It places emphasis on automotive and medical applications and includes case studies and special aspects in embedded system design **System-on-Chip for**

Real-Time Applications Wael Badawy,Graham A. Julien,2002-10-31 System on Chip for Real Time Applications will be of interest to engineers both in industry and academia working in the area of SoC VLSI design and application It will also be useful to graduate and undergraduate students in electrical and computer engineering and computer science A selected set of papers from the 2nd International Workshop on Real Time Applications were used to form the basis of this book It is organized into the following chapters Introduction Design Reuse Modeling Architecture Design Techniques Memory Circuits Low Power Interconnect and Technology MEMS System on Chip for Real Time Applications contains many signal processing applications and will be of particular interest to those working in that community *Embedded System Design: Topics, Techniques and Trends* Achim Rettberg,Mauro Zanella,Rainer Domer,Andreas Gerstlauer,Franz Rammig,2007-06-01 This volume presents the technical program of the 2007 International Embedded Systems Symposium held in Irvine California It covers timely topics techniques and trends in embedded system design including design methodology networks on chip distributed and networked systems and system verification It places emphasis on automotive and medical applications and includes case studies and special aspects in embedded system design <u>Formal Methods and Models for System Design</u> Rajesh Gupta,Paul Le Guernic,Sandeep Kumar Shukla,Jean-Pierre Talpin,2004-06-30 Perhaps nothing characterizes the inherent heterogeneity in embedded sys tems than the ability to choose between hardware and software implementations of a given system function Indeed most embedded systems at their core repre sent a careful division and design of hardware and software parts of the system To do this task effectively models and methods are necessary functionality to capture application behavior needs and system implementation constraints Formal modeling can be valuable in addressing these tasks As with most engineering domains co design practice defines the state of the it seeks to add new capabilities in system conceptualization mod art though eling optimization and implementation These advances particularly those related to synthesis and verification tasks direct1y depend upon formal under standing of system behavior and performance measures Current practice in system modeling relies upon exploiting high level programming frameworks such as SystemC EstereI to capture design at increasingly higher levels of ab straction and attempts to reduce the system implementation task While raising the abstraction levels for design and verification tasks to be really useful these approaches must also provide for reuse adaptation of the existing intellectual property IP blocks *Model-Based Design for Embedded Systems* Gabriela Nicolescu,Pieter J. Mosterman,2018-09-03 The demands of increasingly complex embedded systems and associated performance computations have resulted in the development of heterogeneous computing architectures that often integrate several types of processors analog and digital electronic components and mechanical and optical components all on a single chip As a result now the most prominent challenge for the design automation community is to efficiently plan for such heterogeneity and to fully exploit its capabilities A compilation of work from internationally renowned authors Model Based Design for Embedded Systems elaborates on related practices and addresses the main facets of heterogeneous model based

design for embedded systems including the current state of the art important challenges and the latest trends Focusing on computational models as the core design artifact this book presents the cutting edge results that have helped establish model based design and continue to expand its parameters The book is organized into three sections Real Time and Performance Analysis in Heterogeneous Embedded Systems Design Tools and Methodology for Multiprocessor System on Chip and Design Tools and Methodology for Multidomain Embedded Systems The respective contributors share their considerable expertise on the automation of design refinement and how to relate properties throughout this refinement while enabling analytic and synthetic qualities They focus on multi core methodological issues real time analysis and modeling and validation taking into account how optical electronic and mechanical components often interface Model based design is emerging as a solution to bridge the gap between the availability of computational capabilities and our inability to make full use of them yet This approach enables teams to start the design process using a high level model that is gradually refined through abstraction levels to ultimately yield a prototype When executed well model based design encourages enhanced performance and quicker time to market for a product Illustrating a broad and diverse spectrum of applications such as in the automotive aerospace health care consumer electronics this volume provides designers with practical readily adaptable modeling solutions for their own practice   *Proceedings of ASP-DAC/VLSI Design 2002* ,2002 Papers from a January 2002 conference are organized into four sessions each on low power design synthesis testing layout and interconnects and technology as well as two sessions each on embedded systems verification and VLSI architecture one session on analog design and one session on hot c

   Modeling Embedded Systems and SoC's Axel Jantsch,2003-06-23 Over the last decade advances in the semiconductor fabrication process have led to the realization of true system on a chip devices But the theories methods and tools for designing integrating and verifying these complex systems have not kept pace with our ability to build them System level design is a critical component in the search for methods to develop designs more productively However there are a number of challenges that must be overcome in order to implement system level modeling This book directly addresses that need by developing organizing principles for understanding assessing and comparing the different models of computation necessary for system level modeling Dr Axel Jantsch identifies the representation of time as the essential feature for distinguishing these models After developing this conceptual framework he presents a single formalism for representing very different models allowing them to be easily compared As a result designers students and researchers are able to identify the role and the features of the right model of computation for the task at hand Offers a unique and significant contribution to the emerging field of models of computation Presents a systematic way of understanding and applying different Models of Computation to embedded systems and SoC design Offers insights and illustrative examples for practioners researchers and students of complex electronic systems design   **IEEE Circuits & Devices** ,1996   **15th Symposium on Integrated Circuits and Systems Design** Ricardo Augusto da Luz Reis,Ney Calazans,2002   **ESL Models and their Application**

Brian Bailey,Grant Martin,2009-12-15 This book arises from experience the authors have gained from years of work as industry practitioners in the field of Electronic System Level design ESL At the heart of all things related to Electronic Design Automation EDA the core issue is one of models what are the models used for what should the models contain and how should they be written and distributed Issues such as interoperability and tool transportability become central factors that may decide which ones are successful and those that cannot get sufficient traction in the industry to survive Through a set of real examples taken from recent industry experience this book will distill the state of the art in terms of System Level Design models and provide practical guidance to readers that can be put into use This book is an invaluable tool that will aid readers in their own designs reduce risk in development projects expand the scope of design projects and improve developmental processes and project planning **Behavioral Modeling for Embedded Systems and Technologies: Applications for Design and Implementation** Gomes, Luis,Fernandes, Jo?o M.,2009-07-31 This book provides innovative behavior models currently used for developing embedded systems accentuating on graphical and visual notations Provided by publisher Embedded Systems Programming ,1997

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