

Jean-Michel Redouté  
Michiel Steyaert

**ACSP**  
Analog Circuits And Signal Processing

# EMC of Analog Integrated Circuits

 Springer

# Emc Of Analog Integrated Circuits Analog Circuits And Signal Processing

**George A. Tsihrintzis, Maria  
Virvou, Evangelos Sakkopoulos, Lakhmi  
C. Jain**

## **Emc Of Analog Integrated Circuits Analog Circuits And Signal Processing:**

EMC of Analog Integrated Circuits Jean-Michel Redouté, Michiel Steyaert, 2009-10-10 Environmental electromagnetic pollution has drastically increased over the last decades The omnipresence of communication systems various electronic appliances and the use of ever increasing frequencies all contribute to a noisy electromagnetic environment which acts detrimentally on sensitive electronic equipment Integrated circuits must be able to operate satisfactorily while cohabiting harmoniously in the same appliance and not generate intolerable levels of electromagnetic emission while maintaining a sound immunity to potential electromagnetic disturbances analog integrated circuits are in particular more easily disturbed than their digital counterparts since they don t have the benefit of dealing with predefined levels ensuring an innate immunity to disturbances The objective of the research domain presented in EMC of Analog Integrated Circuits is to improve the electromagnetic immunity of considered analog integrated circuits so that they start to fail at relevantly higher conduction levels than before

*EMC of Analog Integrated Circuits* Jean-Michel Redouté, Michiel Steyaert, 2010-04-29 Environmental electromagnetic pollution has drastically increased over the last decades The omnipresence of communication systems various electronic appliances and the use of ever increasing frequencies all contribute to a noisy electromagnetic environment which acts detrimentally on sensitive electronic equipment Integrated circuits must be able to operate satisfactorily while cohabiting harmoniously in the same appliance and not generate intolerable levels of electromagnetic emission while maintaining a sound immunity to potential electromagnetic disturbances analog integrated circuits are in particular more easily disturbed than their digital counterparts since they don t have the benefit of dealing with predefined levels ensuring an innate immunity to disturbances The objective of the research domain presented in EMC of Analog Integrated Circuits is to improve the electromagnetic immunity of considered analog integrated circuits so that they start to fail at relevantly higher conduction levels than before

Symbolic Analysis of Analog Circuits: Techniques and Applications Lawrence P. Huelsman, Georges Gielen, 2012-12-06 This book brings together important contributions and state of the art research results in the rapidly advancing area of symbolic analysis of analog circuits It is also of interest to those working in analog CAD The book is an excellent reference providing insights into some of the most important issues in the symbolic analysis of analog circuits

*Development, Validation, and Application of Semi-Analytical Interconnect Models for Efficient Simulation of Multilayer Substrates* Renato Rimolo-Donadio, 2011 This thesis deals with the development of semi analytical models for the electrical behavior of vias and traces in chip packages and printed circuit boards A framework for automated simulation of multilayer structures is also proposed The validation and evaluation of the models are thoroughly addressed with several test structures and application studies It is shown that the models can provide good results up to 40 GHz whereas the numerical efficiency is at least two orders of magnitude higher in comparison to general purpose numerical methods

Machine Learning Paradigms George A. Tsihrintzis, Maria Virvou, Evangelos Sakkopoulos, Lakhmi C.

Jain,2019-07-06 This book is the inaugural volume in the new Springer series on Learning and Analytics in Intelligent Systems The series aims at providing in hard copy and soft copy form books on all aspects of learning analytics advanced intelligent systems and related technologies These disciplines are strongly related and mutually complementary accordingly the new series encourages an integrated approach to themes and topics in these disciplines which will result in significant cross fertilization research advances and new knowledge creation To maximize the dissemination of research findings the series will publish edited books monographs handbooks textbooks and conference proceedings This book is intended for professors researchers scientists engineers and students An extensive list of references at the end of each chapter allows readers to probe further into those application areas that interest them most      **Analog Integrated Circuit Design** Tony Chan Carusone,David Johns,Kenneth Martin,2011-12-13 When first published in 1996 this text by David Johns and Kenneth Martin quickly became a leading textbook for the advanced course on Analog IC Design This new edition has been thoroughly revised and updated by Tony Chan Carusone a University of Toronto colleague of Drs Johns and Martin Dr Chan Carusone is a specialist in analog and digital IC design in communications and signal processing This edition features extensive new material on CMOS IC device modeling processing and layout Coverage has been added on several types of circuits that have increased in importance in the past decade such as generalized integer N phase locked loops and their phase noise analysis voltage regulators and 1 5b per stage pipelined A D converters Two new chapters have been added to make the book more accessible to beginners in the field frequency response of analog ICs and basic theory of feedback amplifiers      *Noise Coupling in System-on-Chip* Thomas Noulis,2018-01-09 Noise Coupling is the root cause of the majority of Systems on Chip SoC product fails The book discusses a breakthrough substrate coupling analysis flow and modelling toolset addressing the needs of the design community The flow provides capability to analyze noise components propagating through the substrate the parasitic interconnects and the package Using this book the reader can analyze and avoid complex noise coupling that degrades RF and mixed signal design performance while reducing the need for conservative design practices With chapters written by leading international experts in the field novel methodologies are provided to identify noise coupling in silicon It additionally features case studies that can be found in any modern CMOS SoC product for mobile communications automotive applications and readout front ends      Interconnect-Centric Design for Advanced SOC and NOC Jari Nurmi,H. Tenhunen,J. Isoaho,Axel Jantsch,2006-03-20 In Interconnect centric Design for Advanced SoC and NoC we have tried to create a comprehensive understanding about on chip interconnect characteristics design methodologies layered views on different abstraction levels and finally about applying the interconnect centric design in system on chip design Traditionally on chip communication design has been done using rather ad hoc and informal approaches that fail to meet some of the challenges posed by next generation SOC designs such as performance and throughput power and energy reliability predictability synchronization and management of concurrency To address these challenges it is critical to take a global view

of the communication problem and decompose it along lines that make it more tractable We believe that a layered approach similar to that defined by the communication networks community should also be used for on chip communication design The design issues are handled on physical and circuit layer logic and architecture layer and from system design methodology and tools point of view Formal communication modeling and refinement is used to bridge the communication layers and network centric modeling of multiprocessor on chip networks and socket based design will serve the development of platforms for SoC and NoC integration Interconnect centric Design for Advanced SoC and NoC is concluded by two application examples interconnect and memory organization in SoCs for advanced set top boxes and TV and a case study in NoC platform design for more generic applications

*Analog Signal Processing* Peter B. Aronhime, F.W. Stephenson, 2013-04-09 Analog Signal Processing brings together in one place important contributions and state of the art research results in this rapidly advancing area Analog Signal Processing serves as an excellent reference providing insight into some of the most important issues in the field

*Parasitic Substrate Coupling in High Voltage Integrated Circuits* Pietro Buccella, Camillo Stefanucci, Maher Kayal, Jean-Michel Sallese, 2018-03-14 This book introduces a new approach to model and predict substrate parasitic failures in integrated circuits with standard circuit design tools The injection of majority and minority carriers in the substrate is a recurring problem in smart power ICs containing high voltage high current switching devices besides sensitive control protection and signal processing circuits The injection of parasitic charges leads to the activation of substrate bipolar transistors This book explores how these events can be evaluated for a wide range of circuit topologies To this purpose new generalized devices implemented in Verilog A are used to model the substrate with standard circuit simulators This approach was able to predict for the first time the activation of a latch up in real circuits through post layout SPICE simulation analysis Discusses substrate modeling and circuit level simulation of parasitic bipolar device coupling effects in integrated circuits Includes circuit back annotation of the parasitic lateral n p n and vertical p n p bipolar transistors in the substrate Uses Spice for simulation and characterization of parasitic bipolar transistors latch up of the parasitic p n p n structure and electrostatic discharge ESD protection devices Offers design guidelines to reduce couplings by adding specific protections

**Low-Voltage Low-Power Analog Integrated Circuits** Wouter A. Serdijn, 2012-12-06 Low Voltage Low Power Analog Integrated Circuits brings together in one place important contributions and state of the art research results in this rapidly advancing area Low Voltage Low Power Analog Integrated Circuits serves as an excellent reference providing insight into some of the most important issues in the field

*Computer-Aided Design of Analog Circuits and Systems* L. Richard Carley, Ronald Gyurcsik, 2012-12-06 Computer Aided Design of Analog Circuits and Systems brings together in one place important contributions and state of the art research results in the rapidly advancing area of computer aided design of analog circuits and systems This book serves as an excellent reference providing insights into some of the most important issues in the field

*Analog/RF and Mixed-Signal Circuit Systematic Design* Mourad Fakhfakh, Esteban Tlelo-Cuautle, Rafael

Castro-Lopez,2013-02-03 Despite the fact that in the digital domain designers can take full benefits of IPs and design automation tools to synthesize and design very complex systems the analog designers task is still considered as a handcraft cumbersome and very time consuming process Thus tremendous efforts are being deployed to develop new design methodologies in the analog RF and mixed signal domains This book collects 16 state of the art contributions devoted to the topic of systematic design of analog RF and mixed signal circuits Divided in the two parts Methodologies and Techniques recent theories synthesis techniques and design methodologies as well as new sizing approaches in the field of robust analog and mixed signal design automation are presented for researchers and R D engineers

**Analog Integrated Circuits and Signal Processing** ,1991

Symbolic Analysis for Automated Design of Analog Integrated Circuits Georges Gielen,Willy

M.C. Sansen,2012-12-06 It is a great honor to provide a few words of introduction for Dr Georges Gielen s and Prof Willy Sansen s book Symbolic analysis for automated design of analog integrated circuits The symbolic analysis method presented in this book represents a significant step forward in the area of analog circuit design As demonstrated in this book symbolic analysis opens up new possibilities for the development of computer aided design CAD tools that can analyze an analog circuit topology and automatically size the components for a given set of specifications Symbolic analysis even has the potential to improve the training of young analog circuit designers and to guide more experienced designers through second order phenomena such as distortion This book can also serve as an excellent reference for researchers in the analog circuit design area and creators of CAD tools as it provides a comprehensive overview and comparison of various approaches for analog circuit design automation and an extensive bibliography The world is essentially analog in nature hence most electronic systems involve both analog and digital circuitry As the number of transistors that can be integrated on a single integrated circuit IC substrate steadily increases over time an ever increasing number of systems will be implemented with one or a few very complex ICs because of their lower production costs

*IEEE 2000 First International Symposium on Quality Electronic Design* ,2000

**Distortion Analysis of Analog Integrated Circuits** Piet Wambacq,Willy M.C.

Sansen,2013-04-17 The analysis and prediction of nonlinear behavior in electronic circuits has long been a topic of concern for analog circuit designers The recent explosion of interest in portable electronics such as cellular telephones cordless telephones and other applications has served to reinforce the importance of these issues The need now often arises to predict and optimize the distortion performance of diverse electronic circuit configurations operating in the gigahertz frequency range where nonlinear reactive effects often dominate However there have historically been few sources available from which design engineers could obtain information on analysis tech niques suitable for tackling these important problems I am sure that the analog circuit design community will thus welcome this work by Dr Wambacq and Professor Sansen as a major contribution to the analog circuit design literature in the area of distortion analysis of electronic circuits I am personally looking forward to having a copy readily available for reference when designing integrated circuits for communication

systems     *Recent Awards in Engineering* ,1983     **Symposium Record** ,2002     Modeling and Simulation of Mixed Analog-Digital Systems B. Antao,2012-12-06 Modeling and Simulation of Mixed Analog Digital Systems brings together in one place important contributions and state of the art research results in this rapidly advancing area Modeling and Simulation of Mixed Analog Digital Systems serves as an excellent reference providing insight into some of the most important issues in the field

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